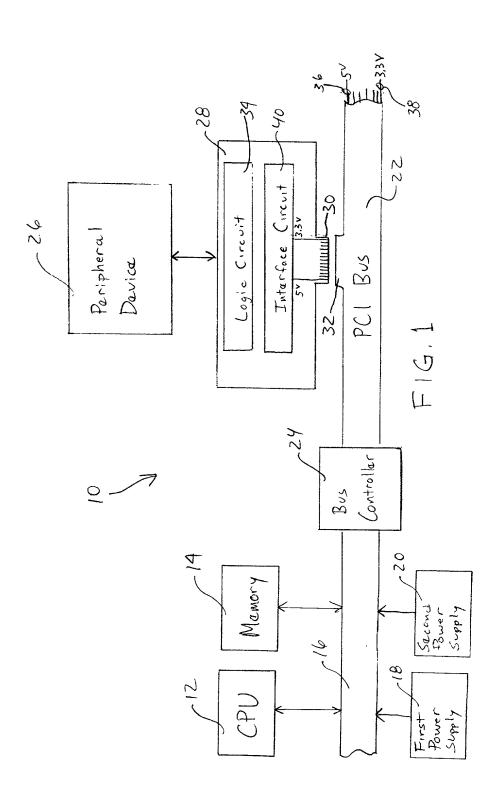
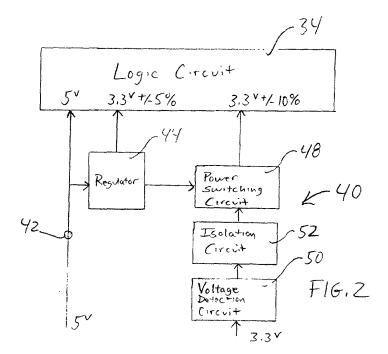
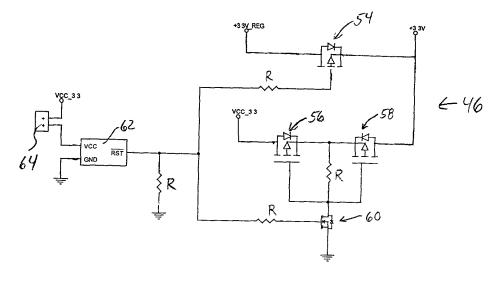
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.1t.e: INTERFACE CIRCUIT FOR PROVIDING A COMPUTER LOGIC CIRCUIT WITH FIRST AND SECOND VOLTAGES AND AN ASSOCIATED METHOD

Inventor(s): Charles Clark Jablonski, et al.

Atty Dkt No: 42626/238154

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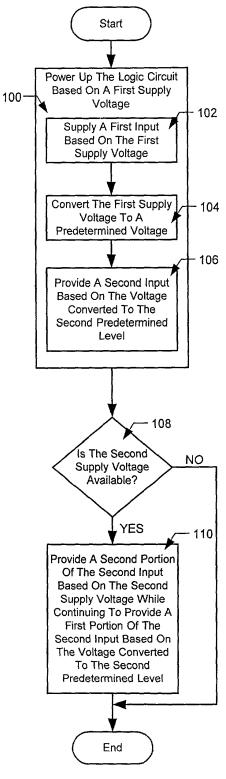


Figure 4